



AGENDA

Twenty-second (22nd) Meeting of SENATE of

Indraprastha Institute of Information Technology, Delhi

Date: 19th June,2013

Day: Wednesday

Time: 02.00 PM

**Venue: Senate Room, B-wing, 5th Floor,
R&D Building, IIIT-D Campus,
Okhla Industrial Estate, Phase-III,
New Delhi-110020**

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TWENTY-SECOND (22nd) MEETING OF SENATE OF IIT-DELHI

AGENDA

22.1 Opening remarks of the Chairman

22.2 Thanks to outgoing members and Welcome of new members. List of Outgoing and New members is placed at **Annexure-1**.

22.3 Confirmation of minutes of the 21st meeting of the Senate:

The minutes of the 21st meeting of the Senate were circulated among the members. No comments have been received so far. The Senate may consider the same for confirmation. A copy of the same is placed at **Annexure-2**.

ACTION TAKEN REPORT

22.4 Revised version of UG Manual:

Being prepared – after including changes in this meeting, a new version will be released before the start of AY2013-14.

22.5 Revised version of PG Manual:

Being prepared – after including changes in this meeting, a new version will be released before the start of AY2013-14.

22.6 Regulations for BTech(ECE)

Revised regulations are being tabled as a separate item.

ITEMS FOR INFORMATION

22.7 Summary of Grade Change information for Winter Semester 2013:

The summary of Grade Change in the recently concluded Winter Semester 2013 is as follows:

Sl.No.	Course	Faculty	No. of students whose grade have been changed
1.	MTH201: Probability & Statistics	Dr. Ponnurangam Kumaraguru	173
2.	COM301: Technical Communication	Mr. Hemant Kumar	19
3.	ESC206: Computational Genomics	Dr. Dhruv Grover	14
4.	CSE330/530: Distributed Systems Security	Dr. H.B. Acharya	05
5.	CSE222: Analysis & Design of Algorithms	Dr. Debajyoti, Dr. Rajiv	03
6.	CSE102: Data Structures and Algorithms	Dr. Mayank Vatsa	03
7.	CSE440: Intelligent Systems-I	Dr. Ashwin Srinivasan	02

22.8 Tentative list of students likely to graduate as on 21 May, 2013.

B.Tech (CSE)

- 50 students of BTech2009 are likely to graduate as on 21 May, 2013. The final verification of requirements of graduation is under process. The tentative list is placed at **Annexure-3**.

M.Tech (CSE /IS)

- 14 Students of MTech2011 are likely to graduate as on 21 May, 2013. The final verification of requirements of graduation is under process. The tentative list is placed at **Annexure-4**.

- **M.Tech (CSE /DE)**

18 Students of MTech2011 are likely to graduate as on 21 May, 2013. The final verification of requirements of graduation is under process. The tentative list is placed at **Annexure-5**.

22.9 Status of PhD/MTech (CSE & ECE) admissions:

➤ **Rolling PhD admissions:**

Following students have been admitted under Rolling PhD:

1. Md. Ayatullah Maktoomi
2. Senthil KK Mani
3. Amit Kumar Chauhan

➤ **Migration from MTech to PhD**

1. Rahul Kumar Mishra
2. Tarun Kumar Bansal
3. Monika Singh
4. Megha Aggarwal

➤ **Regular PhD admissions:**

Based on the entrance examination held on **04th May, 2013**:

- Three candidates were given offer of admission to **PhD (CSE)** and out of which 02 have accepted and deposited the registration charges.
- Six candidates were given offer of admission to **PhD (ECE)** and all of them have accepted and deposited the registration charges.

➤ **M.Tech admissions:**

- Total valid applications received: **1498** (CSE:948, ECE:550)

- Total candidates accepted the direct admission offer to MTech on the basis of GATE score: **49** (CSE:36, ECE:13)
- Total candidates expected to attend written test to be held on 22 June, 2013: **506** (CSE:362, ECE:144)

It is mentioned that the faculty members discussed in their FM held on 3rd April, 2013 a proposal to increase the intake of MTech (ECE) programme from 20 to either 30 or 40. The additional (10 or 20) students will be offered an option of getting 'General' ECE degree. Similarly, 20 seats have been earmarked for MTech (CSE) General. The Senate may kindly consider and approve this proposal.

➤ **B.Tech admissions:**

- Tentative dates for 170 seats (*One supernumerary seat is reserved for Kashmiri Migrants and Ten supernumerary seats are reserved for foreign nationals.*) of B.Tech 2013 admissions:

Release of online application form	7th July 2013 (Sunday)
Last date for online application	17th July, 2013 (Wednesday)
Announcement of Merit List	20th July, 2013 (Saturday)
Counseling	25th July, 2013 (Thursday)
Orientation and registration	1st August, 2013 (Thursday)

- **Admission of Foreign Nationals.**

The Board of Governors has approved 10 supernumerary seats for admission of the foreign nationals; the fee payable is US \$5000 per semester.

22.10 Approval from University Grants Commission (UGC) under section 12(B):
Institute has received approval from UGC under section 12(B). This approval is perpetual and enables institute/faculty members to apply to UGC for various grants.

22.11 Approval from AICTE:

Further approval from AICTE has been received for CSE programmes. However, ECE programmes may get approval next year.

22.12 Academic Calendar for Monsoon Semester 2013:

A copy of the academic calendar for the forthcoming semester is placed at **Annexure-6**

22.13 Information regarding Court Case related to BTech 2013 admission:

The Secretary (Senate) will apprise of the status of the case at the time of the meeting.

ACADEMIC & STUDENTS MATTERS

22.14 MTech graduation requirement: MTech being credit-based requirement at the institute, the students to be awarded the degree as and when they complete the

minimum credit requirements irrespective of their being a TAsip holder or without TAsip.

22.15 Regulations for BTech(ECE)

Regulations based on earlier discussions are given in **Annexure-7**. Some details about credits have been added, based on CSE curriculum. The regulations will be revised based on any decisions in this Senate meeting and released before the start of the academic year.

22.16 To consider having the following requirement for 3rd and 4th year for BTech(CSE) (ECE has already included this): A student must do a total of 32 credits (i.e. 8 full courses, an average of 2 courses per semester) of CSE (or ECE for ECE) courses in the last 4 semesters (without counting IP/IS/UR/BTP). Other courses, particularly from Maths and ECE may also be approved for counting as CSE courses for this requirement. (Earlier we had this requirement stated as 8 credits per semester, but later we allowed IP/IS/BTP to count for at most four, leading to a possibility of reduced course work).

22.17 To consider the possibility of restricting the aggregate credits of IP/IS/UR/BTP Each of IP/IS/UR/BTP offer flexibility and have their own purposes. However, currently, a student can take a maximum of $16+8=24$ credits of IP/IS/UR/BTP, which is equal to 6 full courses. It is proposed to restrict the aggregate credits that can be accumulated through all of these. A limit of 16/20 seems reasonable given that a lot of elective courses that provide structured learning and many a times have significant project component as well. Also, now we will also have ECE electives, some of which will be available to CSE students (and vice versa), and it will be desirable for students to do some of these. (Credits of IP/IS/UR/BRP for the previous batch students are given in appendix as data for discussion.)

22.18 Re-discuss the structure of Minor. Senate has approved Minors earlier – for a minor in a <discipline>, the student must do 12 additional credits, and must do at least 12 credits of courses in <discipline> and should do his/her BTP in <discipline>. It has been pointed out that by requiring the BTP to be done in <discipline> we are effectively disallowing the student to do the BTP in his/her major area – CSE or ECE – and this may not be proper. The requirement for BTP may be dropped, or may be replaced by “student must do at least 4 credits of IP/IS/UR/BTP in <discipline>” (in addition to 12 credits of course work).

22.19 To consider the recommendation of the FM for considering “TA work as a zero credit course: In order to improve TA performance, faculty members in their “Faculty Meeting” held on 20th March, 2013 inter alia, decided to implement a policy in which TA work will be regarded as a ‘0’-credit course. TA work is academic in nature. Thus, no problems are anticipated in including such performance records on transcripts. TA evaluation will have no impact on CGPA.

22.20 To consider items deferred in 21st Senate meeting.

- **Removing "Critical Reading" as a core course (and making it a HSS course.)** That is, instead of 3 courses, the communication skills stream will be two courses. This will free one slot in 2nd year, in which TCOM may be shifted. With this, all core courses will finish by the end of 2nd year, leaving 3rd and 4th year completely for electives, streams, minors.

22.21 Making Software Engineering an elective course for BTech (CSE): Software Engineering was kept as a core course when the BTech program was in IT, where it was felt that special focus on application development must be given. While Software Engineering remains an important topic, it is rarely a core course in any CS program, and it is proposed that, now that our program is in CSE, we make Software Engineering as an elective. However, to ensure that all students get a chance to take this course, Institute should regularly offer this course – perhaps a set of such courses can be identified which, though are electives, are important enough to be offered regularly so all students get a chance to take them.

22.22 Collaboration with Queensland University of Technology (QUT) for a Collaborative PhD programme:

IIIT-D has signed MoU with Australia's **Brisbane-based Queensland University of Technology (QUT)** for a collaborative PhD programme as part of their efforts to promote cooperative educational exchanges.

Under the collaborative PhD programme, the two institutions will jointly develop a system of academic exchanges that will allow a doctoral student to perform coordinated research studies at both institutions with a minimum duration of one and a half year full-time studies spent at each institution.

A PhD student will be jointly guided by supervisors from both institutions with the aim of obtaining PhD by satisfying the requirements and quality standards for the degree of each institution. A single dissertation based on the result of the research undertaken will be submitted for examination. Successful candidates will be eligible for a joint PhD award that names both institutions.

The joint PhD student will be supported financially by a scholarship, stipend or grant from the institutions, or from a third party. The stipend will be commensurate with the standards followed in each institution. Travel costs incurred by the student will also be covered. (Copy of the MoU is placed at **Annexure-8** for information).

22.23 Collaboration with ST Microelectronics:

The institute has entered into agreement with ST Microelectronics, G. Noida under the arrangement, the following four courses for MTech & PhD (ECE) students are being offered during the summer semester 2013:

- Memory Design and Testing:** Mr. Abhishek Lal & Mr. Anuj Grover
- Mixed Signal Design:** Mr. Anurag Tiwari, Dr. Anurup Mitra, Prof R.N.Biswas

- c) **VLSI Design & Test Flow:** Mr. Om Ranjan & Dr. Rajeev Srivastava
- d) **Multimedia Computing:** Kaushik Saha & Mr. Vivek Khaneja

The agreement is placed as **Annexure-9** for information.

22.24 Recommendation/Report by PGC:

22.25 Recommendation/Report by UGC:

The report/recommendations of the UGC are placed at **Annexure-10**

22.26 To consider recommendation for the various Awards/Medals for Graduation for the year 2013:

The recommendation of UGC / PGC are as follows:

- **All round performance medal : Raghav Sethi (2009035)**
- **Best BTP:**
 - Engineering Track: No award to be given, as there was no nomination.
 - Research Track : No award to be given, as the research was not convincing or outstanding by any of the three nominees
 - Entrepreneur Track : None as no BTP was done in Entrepreneur track
- **Chancellor's Gold Medal:** Recommendation awaited.
- **Best MTech Thesis award:** Recommendation awaited.

22.27 Discussions regarding 2nd Convocation and related issues:

22.28 Information regarding Disciplinary action against some students:

Few male students were found using objectionable substance in the hostels. The findings and minutes of DAC are placed at **Annexure-11**.

Subsequently, three students (Shikhar Singhal, Anirudh Chakravorty, Ashish Rawat) have appealed against the decision. The points raised by them have been examined by a sub-committee consisting of Dr. Astrid Kiehn (Chairperson UGC) and Mr. Hemant Kumar (Faculty-in-Charge, Student Affairs). The comments/input of the sub-committee are given in **Annexure-12**.

22.29 Any other item with permission of the Chair.

SENATE MEMBERS
OUTGOING

1	Prof. K.K. Biswas (Re-nominated)	IIT-Delhi	External Member
2	Prof. Dheeraj Sanghi (Re-nominated)	IIT-Kanpur	External Member
3	Prof. K.V. Dinesha	IIIT - Bangalore	External Member
4	Prof. Jayanthi Sivaswamy	IIIT - Hyderabad	External Member
5	Prof. Muthusamy Chelliah	Yahoo !	External Member
6	Mr. Mukesh K. Mohania	IBM	External Member
7	Mr. Sanjay Bahl	Microsoft	External Member
8	Mr. Saugat Sen	Cadence	External Member
9	Mr. C. Anantaram	TCS	External Member
10	Dr. Astrid Kiehn (Re-nominated)	Chairperson UGC	Ex-officio Internal Member
11	Dr. Amarjeet Singh	Faculty - IIITD	Intrnal Member
12	Dr. Debajyoti Bera	Faculty - IIITD	Intrnal Member
13	Dr. Gaurav Gupta	Faculty - IIITD	Intrnal Member
14	Dr. Pushpendra Singh	Faculty - IIITD	Intrnal Member
15	Dr. Saket Srivastava	Faculty - IIITD	Intrnal Member
16	Dr. Shishir Nagaraja	Faculty - IIITD	Intrnal Member
17	Dr. Somitra Sanadhya	Faculty - IIITD	Intrnal Member
18	Dr. Subhasis Banerjee	Faculty - IIITD	Intrnal Member
19	Dr. Vinayak Naik	Faculty - IIITD	Intrnal Member

NEW MEMBERS

1	Prof. K.K. Biswas (Re-nominated)	IIT-Delhi	External Member
2	Prof. Dheeraj Sanghi (Re-nominated)	IIT-Kanpur	External Member
3	Dr. Shreekant Gupta	Delhi School of Economics, Delhi University	External Member
4	Prof. Anshul Kumar	IIT-Delhi	External Member
5	Prof. R.K. Patney	IIT-Delhi	External Member
6	Mr. L V Subramaniam	IBM	External Member
7	Dr. Kaushik Saha	ST Microelectronics	External Member
8	Mr.Srinivas Padmanabhuni	Infosys	External Member
9	Mr. Shubhashis Sengupta	Accenture	External Member

10	Dr. Gautam. Shroff	TCS	External Member
11	Mr. Ramesh Hariharan	Strand Life Sciences	External Member
12	Prof. Samaresh Chatterji	Officiating Dean (Academics)	Ex-officio Internal Member
13	Mr. Hemant Kumar	Officiating Dean (Student Affairs)	Ex-officio Internal Member
14	Prof. Ashwin Srinivasan	Chairperson PGC	Ex-officio Internal Member
15	Dr. Astrid Kiehn (Re-nominated)	Chairperson UGC	Ex-officio Internal Member
16	Dr. A.V.Subramanyam	Faculty-IIITD	Internal Member
17	Dr. Angshul Majumdar	Faculty-IIITD	Internal Member
18	Dr. Dong Hoon Chang	Faculty-IIITD	Internal Member
19	Dr. H.B. Acharya	Faculty-IIITD	Internal Member
20	Dr. Mohd. S Hashmi	Faculty-IIITD	Internal Member
21	Dr. Sanjit K. Kaul	Faculty-IIITD	Internal Member
22	Dr. Shreemoy Mishra	Faculty-IIITD	Internal Member
23	Dr. Srikanta Bedathur	Faculty-IIITD	Internal Member
24	Dr. Sriram K.	Faculty-IIITD	Internal Member
25	Dr. Sujay Deb	Faculty-IIITD	Internal Member



**Minutes of the 21st Senate Meeting of IIIT-D held on February 13, 2013 at 02.00 PM
in Senate Room, B-wing, R&D Building, IIIT-D Campus, Okhla Industrial Estate Phase-3, New Delhi**

Following members were present:

- Prof. Pankaj Jalote - Chairman
- Prof. K.K. Biswas - External Member – IIT Delhi
- Dr. Astrid Kiehn - UGC Chair & Internal Member
- Dr. Amarjeet Singh - Internal Member
- Dr. Debajyoti Bera - Internal Member
- Dr. Pushpendra Singh - Internal Member
- Dr. Saket Srivastava - Internal Member
- Dr. Somitra Kumar Sanadhya - Internal Member
- Dr. Vinayak Naik - Internal Member
- Sh. Ashwani Kumar Kansal - Secretary

Special Invitees:

- Prof. Ashwin Srinivasan - PGC Chair - IIITD
- Prof. Samaresh Chatterjee - Faculty – IIITD
- Dr. Srikanta Bedathur - Faculty – IIITD
- Dr. Shreemoy Mishra - Faculty – IIITD
- Dr. Haimonti Datta - Faculty – IIITD
- Mr. K.P. Singh - In-charge-Academics
- Mr. Vivek Tiwari - DM-Academics
- Mr. Kuldeep Singh - AM-Academics

TWENTYFIRST (21st) MEETING OF SENATE OF IIT-DELHI

MINUTES OF THE MEETING (held on 13th February, 2013)

22.4 Opening remarks of the Chairman

The Chairman welcomed all to the meeting. Thereafter he informed that tenure of some of the Senate members has expired/would be expiring and would take action to replace by new members as per the existing guidelines.

22.5 Confirmation of minutes of the 20th meeting of the Senate

Since there were no comments, the minutes of the 20th meeting were confirmed

22.6 Admission criteria for BTech program for the session 2013-14:

The Chairman Senate apprised the members of the decision taken by the Senate at its 20th meeting (vide item number 20.8) held on 17th October, 2012 and the proposal for giving bonus marks for Olympiad and IIT-D's 'Procon Junior' programming contest. After detailed deliberations, the Senate approved the proposal for allocating the bonus marks as follows:

- a) Bonus marks for **Olympiad only those who are selected for attending the training camp** – 10 marks
- b) Bonus marks for IIT-D's **Programming contest** for schools (Procon Junior) conducted in Oct 2012: Top three candidates – 8 marks and the next seven candidates (i.e. a total of 10 candidates) – 5 marks.

It was also decided that where a candidate has qualified both, he/she will get higher of the two marks. Further, the score for Procon Junior will be valid for two years.

It was decided that admission will be done based on the final score card provided by JEE (Main) (expected around July 07). The weight of JEE(Main) performance and Class XII performance to compute the total score will be as in JEE(Main) (i.e. 60-40). To this score, bonus marks will be added to get the grand total marks, which will be used to prepare the merit list.

Considering the possibility of delay in getting the normalized marks of XII board and the final score card of JEE (Main), it was felt necessary to have alternative arrangement to meet the situation. The Senate therefore decided that in case there is a delay in getting the final score card in which normalized marks of XII exam are also reported, which can impact the starting of the academic session in time, the merit list will be prepared based on the following criteria:

50% of initial JEE (Main) score + 50% of XII board marks + Bonus (if applicable)

As decision whether to opt for this option will have to be taken based on ground realities prevailing at the time, it was decided that it may be taken by the Director, in consultation with the faculty (and reported in next Senate meeting).

The website may be updated with information regarding the admission criteria, but should also clearly mention that “in case there are delays in the declaration of final score card of JEE(Main), the institute, in order to ensure that academic session is not delayed, reserves the right to complete its admission based on initial results of JEE (Main) and class XII scores of candidates”.

22.7 Branch transfer of BTech. students from ECE to CSE and vice versa:

The Senate approved the proposal. The Senate also suggested that UG Committee may organize orientation programme to educate the students.

22.8 To consider certain issues for policy decisions:

- **Allowing a BTP(Industry):** The Senate considered the proposal to allow a student to do BTP in the industry while working full time in the company as an intern and approved the same. The Senate further decided that
 - (i) the BTP done in the industry be called as **BTP (External)** in order to differentiate from the BTP done within the institute and to allow it to be done in other Institutes and research labs.
 - (ii) BTP (External) can be done in one semester with 12-credits instead of 8-credits that are normally allowed. It is expected that during this BTP, the student is not doing course work, but may be allowed to register for some minimal credits, if it is needed to facilitate him/her doing it in some organization (e.g. in some research organizations/universities, a student can come only if he registers for some credits.) Necessary changes will be made in the regulations for this.
 - (iii) The organization in which the student undertakes the BTP will have to be approved by the UGC, which may, in due course also evolve a list of pre-approved organizations.

- **Making "Critical Reading" a HSS course:** The Senate discussed the proposal at length. There was no agreement on the proposal and some members had the view that it should remain as is. However, it was also pointed out that in absence of any institute faculty the course cannot be run as a core course. This should be discussed again in the senate.

- **Having refresher modules in summer for selected core courses (e.g. DSA and OS) using online resources and TAs:** The Senate considered the proposal. In the light of the views expressed by the members, the Senate decided that the UG Committee may offer such courses with suitable

safeguards to ensure that assessment and grading is fair. UGC may evolve further guidelines for implementation.

- There is also a possibility of offering a 2-credit module for MTech students interested in teaching careers on "Teaching Computer Science" where there is some self study (or internet based lectures) material on pedagogy etc, and teaching the refresher module is their "practice/lab". This was not discussed and the topic was deferred.

22.9 To apprise the Senate of the Course Registration status:

The Senate noted the status.

22.10 List of students who completed graduation requirements on 21st Dec., 2012.

The list was approved and recommended to BOG for grant of Degree.

22.11 To report the admission of new students in PhD program:

The Senate accepted the students.

22.12 To report the students who are on semester leave during Winter Semester 2013:

The Senate noted the position. The Senate also observed that PGC is empowered to grant permission for Semester Leave.

22.13 To consider the proposed calendar for Summer Term:

The Senate approved the calendar in principle.

22.14 To consider and approve the revised version of UG Manual:

Consideration of this item was deferred. The Senate desired to obtain input from UGC.

22.15 To consider and approve the revised version of PG Manual:

Consideration of this item was deferred. The Senate desired to obtain input from PGC.

22.16 Recommendation/Report by UGC: None.

22.17 Recommendation/Report by PGC: None.

22.18 Any other item with permission of the Chair: None

BTech2009		
Sl.No.	Roll No.	Name
1	2009001	Aadish Gupta
2	2009003	Kumar Abhinav Kapoor
3	2009006	Abhishek Singh
4	2009008	Akashdeep
5	2009009	Akshit Nanda
6	2009011	Anish Kumar
7	2009013	Anshul Tayal
8	2009014	Anuj Sharma
9	2009015	Anupam Singhal
10	2009018	Chirag Gupta
11	2009019	Daksha Yadav
12	2009021	Jatin Kumar
13	2009022	Lakshay Gupta
14	2009023	Lakshay Pandey
15	2009025	Mayank Pundir
16	2009026	Mudita Khurana
17	2009027	Naman Kohli
18	2009028	Naved Alam
19	2009029	Nikhil Mahajan
20	2009030	Nikita Dagar
21	2009031	Nishtha Khanna
22	2009032	Pranav P Raj (Expected to graduate on 21 December, 2013)
23	2009033	Prashant Singh
24	2009034	Priyanshi Mittal
25	2009035	Raghav Sethi
26	2009036	Rahul Bhatnagar
27	2009038	Ramjot Singh Sandhu
28	2009039	Rohit Dixit
29	2009040	Rushil Khurana
30	2009041	Sanchit Garg
31	2009042	Saurav Bose
32	2009043	Shashwat Goel
33	2009044	Shiva Lawaria
34	2009047	Surbhi Jain
35	2009048	Suvan Raj
36	2009049	Swetank Kumar Saha
37	2009050	Tarang Chugh (Expected to graduate on 21 December, 2013)
38	2009051	Tarun Vashisht
39	2009052	Vani Sivasankar
40	2009053	Varun Gandhi
41	2009054	Varun Sachdeva
42	2009055	Aayush Jain
43	2009057	Akash Vanjani
44	2009058	Mannika Solanki
45	2009059	Mehak Soni

46	2009062	Sanchit Sharma
47	2009064	Apurv Mehra
48	2009065	Divya Bansal
49	2009066	Saloni Jain
50	2009067	Srishty Grover
51	2009068	Sudip Mittal
52	2009069	Surabhi Kabra

BTech2008		
Sl.No.	Roll No.	Name
1	2008004	Adesh Verma (expected to graduate on 21 August, 2013)
2	2008036	Naman Ladha (expected to graduate on 21 August, 2013)

Annexure-4

MTech 2011 batch
Specialization: Information Security

1	MT11001	Abhishek Kumar
2	MT11002	Arpan Jati
3	MT11003	Arun Jindal
4	MT11004	Megha Agrawal
5	MT11005	Monika Singh
6	MT11006	Piyush Yadav
7	MT11007	Prachi Jain
8	MT11010	Snigdha Samantaray (expected to graduate on 21 August, 2013)
9	MT11011	Sonia Soubam
10	MT11012	Srishti Gupta
11	MT11013	Sumesh R
12	MT11014	Tarun Bansal
13	MT11015	Vartika Srivastava
14	MT11016	Vidushi Chaudhary
15	MT11017	Vivekanand Agarwal

Annexure-5

Specialization: Data Engineering		
1	MT11018	Abhishek Kumar Singh
2	MT11019	Aditi Sharma
3	MT11020	Ankita Likhyan
4	MT11021	Annapurna Samantaray
5	MT11022	Deepti Azad
6	MT11023	Kritika Rani
7	MT11024	Megha Gupta
8	MT11025	Neha Bansal
9	MT11026	Prabha Rathinam M.
10	MT11027	Radhika Tayal
11	MT11028	Rahul Mishra
12	MT11029	Rajeev Kumar
13	MT11030	Satadipa Chakraborty
14	MT11031	Saurabh Shakyawar
15	MT11032	Shuchi Mala
16	MT11033	Sruti Sharma
17	MT11034	Swati Agarwal
18	MT11035	Vandna Sharma

Annexure-6

Academic Calendar (Academic Year 2013-14)

Monsoon Semester 2013 w.e.f. August 5, 2013

Course Registration & Commencement of Classes	05 August, 2013 (Mon)
Last day for Late Registration	08 August, 2013 (Thu)
Last day for add/drop courses	12 August, 2013 (Mon)
ESYA Tech. Fest of IIIT-D	16-17 August, 2013 (Fri-Sat)
2 nd Convocation	24 August, 2013 (Sat)
Mid-semester examinations week	23-28 September, 2013
Diwali Break	04 - 09 November, 2013
BTP Submission	10 November, 2013 (Sun)
BTP Presentation	16 November, 2013 (Sat)
Last day of classes	22 November, 2013 (Fri)
Exam preparation (no classes)	23 – 24 November, 2013
End-semester examinations week	25 – 30 November, 2013
Demos & Project presentations	01 - 04 December, 2013
Moderation Meeting	06 December, 2013 (Fri)
Announcement of Grades	09 December, 2013 (Mon)

Time Table (TT) adjustments:

- 1) *On 10th August (Sat) Time Table of **FRIDAY** applies.*
- 2) *On 19th August (Mon) Time Table of **TUESDAY** applies.*
- 3) *On 15th October (Tue) Time Table of **WEDNESDAY** applies.*

List of Gazetted Holidays (GH)

Eid-Ul-Fitr*	09 August, 2013 (Fri)
Independence Day	15 August, 2013 (Thu)
Raksha Bandhan	20 August, 2013 (Tue)
Mahatma Gandhi's Birthday	02 October, 2013 (Wed)
Dussehra (Vijay Dashmi)	13 October, 2013 (Sun)
Eid-Ul-Zuha (Bakrid)*	16 October, 2013 (Wed)
Diwali	03 November, 2013 (Sun)
Bhai Dooj	05 November, 2013 (Tue)
Guru Nanak's Birthday	17 November, 2013 (Sun)

Annexure-7

Regulations for BTech in Electronics and Communications Engineering (ECE)

1. Preamble

IIIT-Delhi's objective is to become a globally respected Institution for research and education in IT and allied areas. Information Technology is a broad area which uses Computing and Communication technologies to solve problems. The two major disciplines to nurture these are Computer Science and Engineering (CSE) – which covers computing science and its applications, and Electronics & Communication Engineering (ECE) – which covers science and engineering of communication technologies and computing systems. This note gives the regulations for the BTech(ECE) program. (General requirements for the BTech programs are defined in the UG Manual of the Institute).

In order to give due importance to applied as well as theoretical aspects of ECE, the curriculum for the BTech in Electronics and Communications Engineering (ECE) program covers most of the foundational aspects and also develops in students the engineering skills for problem solving. Towards this, the BTech (ECE) program at IIIT-Delhi starts with computing and Electronics courses first, and allows the possibility of doing science courses later. Besides being better suited for developing engineering capabilities, it also enables the possibility of students seeing newer applications and possibilities of using computing and electronics in these subjects.

With this approach, the BTech (ECE) program can be divided broadly in two halves. The first half will focus on building the foundations, and will be highly structured. The second part is for developing the skills and knowledge of the students in various topics – computing and application domains. This part also provides limited specializations, and different students may follow different paths and take different set of courses in it.

The first year program is common with CSE – this allows flexibility to students in moving from one to the other. After the first year common program, the second year program is relatively fixed, comprising mostly of core courses for the program. 3rd year onwards the program can be mostly flexible comprising of electives, which may be organized as streams.

2. Program Structure

2.1 Common 1st year for both ECE and CSE Student

The common first year program is given below. Each of the two semesters, all students do one course each in maths, software, hardware, systems/practice, and HSS/Comm. All courses, except HSS, are core courses.

	Sem 1	Sem 2
Hardware	Digital circuits	Computer Organization
Software	Intro to Programming	Data structures and Algos
Systems/Practice	System Management	Intro to Engineering Design
Maths	Math 1 (Linear Algebra)	Math 2 (Probability and Statistics)

Communication Skills / HSS	Com 1	HSS 1
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2.2 Core Courses for ECE in 2nd Year

- 2.2.1 In 3rd and 4th semesters, the course load will be same as in CSE – 5 courses, including one HSS/Communication skills. These core courses are given below: These courses will become pre-requisites for advanced courses in some of the streams. (The semester mentioned for the core courses is indicative and suggested, and they can be done later/earlier also. However, the pre-requisite requirements must be kept in mind by a student, if he/she wishes to do a core course in some other semester.)

Sem 3	Sem 4
Linear Circuits	Principles of Commn. Systems
Embedded Logic Design	Integrated Electronics
Signals & Systems	Fields and Waves
Math 3 (Complex Variables, Vector Calculus, and ODEs)	Math 4 (Numerical Methods and Optimization)
COM course	HSS 2

2.3 Program Structure in 3rd/4th Years

- 2.3.1 Most courses in Sem 5-8 are electives (an elective course is one which is not compulsory, and a student may have choices from which to select the courses he/she wants to do.)
- 2.3.2 Some of the electives may be organized as *streams*, where a stream is a sequence of courses in an area providing a limited specialization in that area. Streams allow the student to focus on some area of ECE – as ECE is too diverse it is not possible for a student to gain a decent mastery in all. Streams allow the student to focus on a chosen area to gain a level of depth, and gain some understanding of other areas by doing some courses from other streams. A student will be strongly encouraged to ensure that at least one stream is completed, though is not required to do so.
- 2.3.3 Besides electives and streams for specialized areas, streams and electives from domain areas (e.g. health, life sciences, finance, economics, E-Governance, sciences, etc.) may also be offered.
- 2.3.4 There will also be a set of Humanities and Social Sciences (HSS) courses offered.

3 Requirements for Graduation

For a BTech(ECE) degree, a student must satisfy all the following requirements:

- 3.1 Earn a total of 152 credits (equivalent to 38 full courses – 20 courses in the first two years, and 18 courses in the last two years.)
- 3.2 Successfully complete all the core courses.
- 3.3 Do at least 12 credits of Humanities and Social Sciences Courses.

- 3.4 Do 2 credits of Community Work and Self Development each. These are pass/fail credits, which are required to be completed, but do not count for fulfilling the credit requirement (i.e. these are in addition to the requirements mentioned above)
- 3.5 In the last 4 semesters, do at least 32 credits (8 full courses) of ECE courses.
- 3.6 A BTech Project (BTP) is optional. A student opting for BTP, may take a total of 8 to 16 credits of BTP. In a semester, the student can normally register for at most 8 credits of BTP.
- 3.7 A student may take “Independent Project” or “Independent Study” or “Undergraduate Research” courses for 1, 2, or 4 credits. No more than 8 credits of these courses combined may be taken in the entire program. Only students with satisfactory CGPA (at least 7.5) or with a strong interest in some area (the faculty advisor to determine this) can take these courses.

4 Honors Program

The BTech(ECE) program has the Honors option, requirements for which are same as specified in the regulations for the BTech program. Namely;

- 4.1 The student must earn an additional 12 credits (i.e. must complete at least 164 credits)
- 4.2 The student’s program must include a BTech Project
- 4.3 At graduation time, the student must have a CGPA of 8.0 or more

5 Suggested “Streams” for ECE

Streams will evolve over time – based on trends as well as faculty interests. Some common streams that are possible are given below. Then nature of streams will evolve with time depending on the interests of faculty as well as relevance/importance of the areas.

- 1. Signal & Image Processing
- 2. Communications Systems/Networking
- 3. Circuits and Systems
- 4. Computer Engineering
- 5. RF and Applied Electromagnetics
- 6. Embedded Systems/Controls

The courses in the streams will be of two types to serve dual purpose. On one hand there will be courses to train students in the fundamentals of the specific streams whereas on the other hand there will be advanced courses to train the students on the latest developments in those specific streams.

The suggested courses for some of the streams are given below. These courses can be added / deleted based on their relevance after taking feedback from industry and academia.

Signal and Image Processing Courses: Digital Signal Processing, Advanced Signal Processing, Multimedia Communication, Image Processing, Media Security

Communications Systems and Networking Courses: Computer Networks, Ad-hoc Wireless Networks, Probability and Random Processes, Digital Communication, Information Theory and Coding, Wireless Communications

Circuits and Systems Courses: Introduction to VLSI Design, Introduction to Microelectronics, Analog IC Design, Digital IC Design, System on Chip Design, Design for Testability, Digital Hardware Design

Computer Engineering Courses: Computer Architecture, Operating System, Introduction to Cryptography, Applied Cryptography, Digital Hardware Design

RF and Applied electromagnetics Courses: Antenna and Wave Propagation, Topics in Mathematics, , Radar Systems and Theory, RF Circuit Design, System Design for Wireless, Wireless Communication

Embedded Systems and Control Courses: Embedded Systems, Operating System, Robotics, Control Systems, Multi-agent Systems



Joint PhD Programs

Contents

PART A - Overview

PART B - Overarching Joint PhD Agreement template to be prepared initially

PART C - Checklist to be used in developing a Joint PhD Program for each individual student wishing to enroll in the Joint PhD Program.

PART A - Overview of Joint PhD* requirements

QUT actively promotes joint PhD Programs with leading research universities for suitably qualified candidates. Features of the joint PhD Programs include the following:

1. Simultaneous enrolment at QUT and at one of our partner universities enables students to submit one and the same thesis for recognition by both universities. **At least one third of the student's candidature (one year full-time minimum) must be spent at each university.** Students are supervised by researchers at the two universities and must meet all the requirements to graduate at both of them.
2. At QUT, both inbound (i.e. student from an overseas university) and outbound (i.e. student at QUT) students may be considered for participation in joint PhD Programs. Students seeking to undertake a joint PhD must **meet the admission requirements for both universities and the requirements under the Joint PhD Agreement.**
3. Prior to admitting any student to a joint PhD Program an Overarching Agreement (see Part B), that **meets mutual institutional requirements**, must be negotiated between the two universities.
4. In addition to the usual candidature admission processes and procedures for each Joint PhD student, part C of the Agreement must be agreed upon before student commencing his/her joint enrolment.
5. To progress such an Agreement, an **existing Memorandum of Understanding/ International Cooperation Agreement must be in place** between QUT and the Partner University. This Memorandum of Understanding/ International Cooperation Agreement must include a mutual commitment to the development of research collaboration and exchange.
6. QUT policies that apply to candidature in a Joint PhD Program include:

QUT PhD Regulations (http://cms.qut.edu.au/data/assets/pdf_file/0020/221564/PhD-Course-Regulations-UAB-approved-27-July-2012.pdf),

QUT Code of Conduct for Research (http://www.mopp.qut.edu.au/D/D_02_06.jsp),

Code of Good Practice for Postgraduate Research Studies and Supervision at QUT (http://www.mopp.qut.edu.au/D/D_05_04.jsp),
Intellectual Property Policy (http://www.mopp.qut.edu.au/D/D_03_01.jsp),
Statement of Infrastructure Provision for QUT Postgraduate Research Students (<http://www.qut.edu.au/research/life-as-a-researcher/training-support-and-facilities/>), and
QUT Research Training Quality Assurance Systems
(http://www.rsc.qut.edu.au/pdfs/Policy_and_Procedure/RTQAS5.pdf)

7. The effectiveness of Joint PhD Programs will be reviewed at QUT on a five yearly cycle as part of the review of international Memorandum of Understanding/ International Cooperation Agreements. The review administrator is the Secretary, QUT Research Degrees Committee.

* Joint PhD is a generic term including Double PhD, Double Doctorate, Dual Doctorate, Cotutelle
CRICOS No 00213J



Part B- Overarching Agreement for Joint PhD Programs

Between
Indraprastha Institute of Information Technology (IIIT), Delhi
and

Queensland University of Technology

Preamble

In consideration of:

- laws and regulations governing the cooperation between public institutions of a scientific, cultural and professional nature and other public or private organisations in India ;
- articles in India;
- the regulations governing joint PhD at the IIIT-Delhi.

The IIIT-Delhi with located at Okhla Phase III, New Delhi – 110020, represented in this Agreement by its Director,
and

the Queensland University of Technology (QUT)

located at 2 George Street, Brisbane, Queensland 4001, Australia represented by the Dean of Research and Research Training, agree to jointly develop a system of academic exchanges that will allow for a joint PhD student:

- to perform coordinated research studies at both institutions with a minimum total duration of one year fulltime studies spent at each institution;

- to be jointly guided by supervisors from both institutions with the aim of obtaining the joint PhD by satisfying the requirements and quality standards for the degree of each institution.
- to submit one single thesis based on the result of the research undertaken in both institutions, for examination to each institution

This Agreement is based on the principles and articles specified in this document. It refers specifically to the joint PhD between QUT and the <name of the institution>.

General Principles and Articles of the Agreement

Admission

A candidate wishing to gain admission to the joint PhD must be admitted separately by each institution subject to the following:

- Where the candidate is not yet enrolled into a PhD program of either institution.

If the candidate gains admission to the PhD program of QUT, QUT may recommend acceptance of the candidate into the PhD program of IIIT-Delhi. The IIIT-Delhi may, at their discretion, either accept the recommendations or may require the candidate to attend an interview. Acceptance will be subject to satisfactory performance in this interview.

- Where the candidate is already enrolled in a PhD of either institution and has at least 50% of the candidature remaining:

The candidate may make an application to both institutions to transfer into the joint PhD accompanied by a recommendation from QUT. The IIIT-Delhi may, at their discretion, either accept the recommendation or otherwise may require the candidate attend an interview. Acceptance will be subject to satisfactory performance in this interview.

Language requirements

The joint PhD candidate must demonstrate proficiency in the English language to a standard at least equivalent to:

- IELTS - overall score of 6.5 with no sub-score less than 6; or
- TOEFL (internet-based) – overall score 90 with no sub-score below 20

Other language tests need the approval of the Dean of Research and Research Training at QUT

Duration of the Joint PhD

The total combined duration of a given joint PhD fulltime studies at both institutions will be three years for the PhD unless circumstances justify an extension or reduction subject to the Agreement and approval of both institutions. In the particular case where the specified aims and objectives of the joint degree have not been met, the duration of the studies may be extended in accordance with the regulations of both institutions for PhD.

Residency

The research and study activities will be carried out at both institutions and the minimum total duration of the periods spent at each of the two institutions will be one year. The periods of study may comprise several shorter periods alternating between the institutions or two single periods spent at each institution subject to the Agreement of both supervisors. Visits to institutions other

than those which are party to this Agreement may be authorised if it is considered by both supervisors to be beneficial to the studies.

Enrolment

The minimum academic entry requirements for students intending to apply to undertake the joint PhD will be qualifications acceptable to each institution as set out in their rules.

Supervision

Each institution will appoint a suitably qualified academic as thesis supervisor. Within the agreed timetable, the joint PhD student and supervisors will develop the research proposal in accordance with the requirements of the PhD of both institutions which, as a minimum, will include:

- a) title of the research project;
- b) aims and objectives;
- c) description of the proposed project and work activities;
- d) project milestones;
- e) methodology; and
- f) references.

The joint PhD student will consult both supervisors for advice and assistance and the two supervisors will jointly exercise the function of scientific guide of the joint PhD student and will be jointly responsible for the student's education and research training.

Periodic evaluations

Each institution will advise the other of their particular requirements for the periodic evaluation of the progress of the joint PhD student including, where appropriate, the requirement to sit examinations. The supervisors will agree to a timetable to meet these requirements and advise the student accordingly, who will then comply in all respects with the specified requirements. The progress report schedule is specified as per course rules of each institution with respect to their joint PhD degree.

Academic training

The main academic activity for the joint PhD student is the completion of the research work and the writing and defending of one single thesis for submission to both institutions. In addition, the joint PhD student may take academic courses and associated exams at each of the two institutions in accordance with a study curriculum agreed with the supervisors and complying with the requirements of the joint PhD of each institution.

The student will fulfil all requirements and regulations governing both institutions and their joint PhD. In particular, the student will comply with the course credit recognition requirements/ advanced standing of the joint PhD of each institution and with their examination requirements where this is relevant.

Thesis

By virtue of the international nature of this joint program, the resulting joint PhD thesis is expected to reflect a global perspective and value of the international experience greater than that attainable by work performed independently within either of the individual PhD undertaken in each of the institutions. The single thesis will be written in English with an extended summary in the language of each institution and will be submitted to the respective boards of each institution for examination as per their rules and regulations.

The joint PhD thesis will be submitted to both supervisors prior to final submission, who will assess it for suitability and quality. The thesis will be evaluated by external examiners, appointed and approved by both academic units at each institution. The examiners will submit a written assessment of the thesis with a recommendation that the thesis be passed (no correction, minor corrections or major corrections), be resubmitted once it has been revised, or be failed.

The statement of Original Authorship must state that *“The work contained in this joint PhD thesis undertaken between QUT and IIIT-Delhi has not been previously submitted to meet requirements for an award at these or any other higher education institutions. To the best of my knowledge and belief, the thesis contains no material previously published or written by another person except where due reference is made.”*

Public oral examination as required by IIIT-Delhi

Upon acceptance of the thesis, the candidate will undergo a public oral examination by the final evaluation committee in accordance with the governing regulations of the IIIT-Delhi. Unless otherwise agreed by all parties in writing, the defence of the thesis will take place at the IIIT-Delhi. The defence will be in English or in the language of one of the two Institutions by agreement between the evaluation committee and the joint PhD candidate. The oral examination will take place within four months of submission of the thesis.

Composition of final evaluation committee IIIT-Delhi

QUT’s PhD regulations stipulates that *“there will be one examination process which will be set out in the Joint PhD Agreement and both institutions will be bound by the outcome.”* To this end, some harmonisation of examination process between QUT and IIIT-Delhi may be required.

The final evaluation committee will be formed from an equal number of scientific representatives from the two Institutions. The committee will comprise a minimum of four members, two of whom will be the external reviewers of the thesis for QUT’s examination process. The final evaluation committee will be appointed in accordance with the governing regulations of the two institutions and with the approval of the responsible boards at QUT and at the IIIT-Delhi. All members of the final evaluation committee will participate in person (also means via videolink) in the final oral examination and the defence of the thesis.

Final grading

IIIT-Delhi

If applicable: Upon completion of the defence, a single final grade will be determined by taking into consideration the individual grades of the courses undertaken during the degree, grade for the thesis and oral examination. The thesis and the oral examination will be graded using the University’s standard grading system.

QUT

Under the Australian system PhD theses are not graded. However at QUT, in recognition of outstanding contribution to knowledge and/or demonstration of excellence in postgraduate research, the PhD thesis may be recommended for the QUT Outstanding Doctoral Thesis Award, as per established guidelines (http://www.rsc.qut.edu.au/pdfs/examinations/Outstanding_thesis_awards.pdf).

Award of the degree

After the student has completed the joint PhD, satisfied all the relevant requirements of each institution, and received a positive assessment of the thesis work by the evaluation committee, the student will be awarded a single degree with degree certificates/testamurs from each institution. **The degree certificate/testamur of each institution must attest to the joint framework under which the student completed the joint PhD program between QUT and IIIT-Delhi.** The following words must appear on each institution's testamur: <Student's Name> having fulfilled all the conditions of a joint program prescribed by the Queensland University of Technology and the *IIIT-Delhi in India* has been admitted to the degree of Doctor of Philosophy" or other such words as agreed by both institutions. Appendix one in this Agreement details the example of a testamur which cross reference to the joint PhD program framework. The academic transcripts of both institutions (where applicable) must also state that the degree was undertaken as a consequence of a joint PhD between QUT and IIIT-Delhi. A decision by one university not to award the degree does not preclude the other university from awarding the degree of that university with their standard testamur.

Research results protection

The protection of the thesis content and the publication and exploitation of the research results and acknowledgement of the research scientists involved will be subject to the regulations of both Institutions.

If required, specific agreements will be arranged for the protection of the intellectual property by the appropriate university entities that are formally responsible for this. The policies of QUT relating to Intellectual Property are outlined in the Manual of Policies and Procedures: (http://www.mopp.qut.edu.au/D/D_03_01.jsp).

Administrative matters

Each Institution will designate one individual to fulfil the responsibilities of Coordinator of the Joint PhDs in order to facilitate student exchanges under the terms of this Agreement. At QUT, the office of the Director of International Graduate Research will be the responsible body. The Coordinator will exchange all relevant information about the joint PhD offered at their respective institution and provide the necessary assistance in respect of the administration of this Agreement.

Tuition fees and other financial matters

The joint PhD student is expected to be financially supported by a scholarship, stipend or grant from either one of the institutions, or from a third party. QUT may provide the tuition fee sponsorship (scholarship), if required, while the student is enrolled at QUT. The sponsorship of a tuition fee is subject to satisfactory academic progress and governed by the *Conditions of Award* of tuition fee scholarship. Students will need to satisfy eligibility criteria for the award of a tuition fee scholarship. The joint PhD student will be responsible for meeting the cost of accommodation, living expenses and travel costs. Faculties, at their discretion, may provide travel support for their joint PhD students.

The IIIT-Delhi will waive any bench or tuition fees and other costs and charges. Each institution will make freely available its own resources, laboratories and the like for the joint PhD student's academic activities.

The travel expenses for the two thesis supervisors will be met by the respective supervisors' own research funds. The travel expenses for the other members of the final evaluation committee will be covered by the research funds of the supervisor from the IIT-Delhi. There are no other financial charges for the institutions to consider.

Health insurance and protection measures

The joint PhD student will be required to show proof of health insurance for the required duration, consistent with visa conditions, before assuming residency at either institution.

Disputes

The parties to this Agreement undertake to solve in a friendly manner any dispute arising from the implementation and interpretation of this Agreement.

In the event that the dispute cannot be resolved satisfactorily, the issue(s) will be submitted for arbitration. Each party will appoint a member of the arbitration panel and an additional member will be chosen by mutual agreement between the parties. The dispute resolution is in English and that each party bear own costs and share the costs of the mediator.

Infrastructure support

Normal infrastructure support arrangements will be provided by each institution, consistent with those provided to joint PhD candidates at that institution and any relevant regulations which address this issue. QUT's Statement of Infrastructure Provision for Postgraduate Research Students is at:

http://www.rsc.qut.edu.au/pdfs/Policy_and_Procedure/infrastructure_statement.pdf

Renewal, termination and amendment

This Agreement will remain in force for a period of 5 years from the date of the last signature, with the understanding that it may be terminated by either party giving six months prior notice to the other party, in writing. The Agreement may be extended by mutual consent of the two institutions.

This Agreement may be amended by the exchange of letters between the two institutions. Such amendments, once approved by both institutions, will become part of this Overarching Agreement for Joint PhD Programs.

Liability

Notwithstanding the shared commitment of the parties to amicably resolve disputes to mutual satisfaction, each party will indemnify the other against any liability arising from its breach of commitment under this Agreement. The party offering the indemnity may take over the legal defence of the relevant claim.

Signatories to the Agreement

Queensland University of Technology
Professor Paul Burnett
Dean of Research and Research Training

Brisbane, [date].....

IIIT-Delhi

Name of the authorising person

<insert name of City>,
[date].....



Appendix 1

<Name>

having fulfilled all the conditions of a joint program prescribed by the Queensland University of Technology and the *IIIT-Delhi in India* has been admitted to the degree of”

Doctor of Philosophy

Ms Shard Lorenzo
Registrar

Professor Peter Coaldrake
Vice-Chancellor

Mr Timothy Fairfax
Chancellor

QUT Seal

Conferral date and student ID

Joint PhD Programs



PART C – Checklist

This checklist is primarily intended for use by QUT proponents of joint PhD Program e.g. prospective supervisor or student; but it may be useful for partner universities in identifying key issues for resolution.

Process:

1. Please record the relevant details into the spaces below and discuss these with your colleagues at the partner university.

2. When the checklist has been circulated electronically between both universities and is agreed, please fax it to the QUT Dean of Research and Research Training at +61 7 3138 1304 or send a pdf copy to paul.burnett@qut.edu.au and send the hard copies by courier to *Professor Paul Burnett, Dean of Research and Research Training, Queensland University of Technology, Level 4, 88 Musk Avenue, Kelvin Grove Campus, Qld., 4001, AUSTRALIA.* Please attach correspondence indicating the particular university’s Agreement or have the checklist counter-signed by the administrative contact.

3. The QUT Joint PhD Agreement will be finalised by the Research Students Centre and the administrative contact at the partner university.

Should some of the locked text need to be varied please use the last field (“supplementary comments”) and send the electronic version to the address above for insertion into the Joint PhD Agreement.

Name of partner university	
Address of partner university	
Country of partner university	
Administrative Contact Person at partner university	Name:
	Address/ Contact Details:
Full name of student and academic qualifications	
Gender	
QUT Academic unit (Discipline/ School/ Faculty)	
Partner academic unit (Department/ School/ Faculty)	
Date of start of joint enrolment	
Duration of joint enrolment	
Proposed period of residency in each institution- (At	

least one third of the student's candidature must be spent at each university.)	
Provisional title of thesis/ Research topic	
Specific provisions and requirements of partner university to be incorporated at Sections 16 of the Joint PhD Agreement, e.g. scholarships, stipends, travel costs, consumables, laboratory expenses.	
QUT supervisor (must be accredited)	
Partner University supervisor	
Partner higher degree by research title	
Proposed language of thesis (extended abstract in language of each institution required)	
Proposed examination process and funding arrangements to support this process. (For example minimum QUT requirement for a PhD thesis: Two examiners <ul style="list-style-type: none"> ○ external to both participating institutions, ○ no prior association with the candidature ○ prepared to submit written examiners reports within two months) 	
Is an oral examination required by the partner university?	
Other Comments regarding conditions of candidature, if necessary. While QUT conditions normally apply, the regulations pertaining to candidature at the participating institutions must be compatible. Negotiation may be needed to clarify such matters, including: Eligibility for admission; Scope and detail of preliminary research proposal; Ongoing assessment of progress; Intellectual property/copyright/authorship arrangements; Commercialisation of research outcomes; Ethics approval; OHS & EEO protocols; Variations to candidature (e.g. intermission, withdrawal; change of supervisor); Grievance procedures; Termination and discipline/research misconduct procedures	
Supplementary comments. Please use this space to record items that need to be amended in the 'locked' text. These amendments will be dealt with at QUT.	

Signatures' Page

Approvals	Signature/Date
Supervisors: Name of QUT supervisor: Name of Partner University supervisor	
Name of the Relevant Faculty HDR Coordinator:	
Name of the QUT Dean of Research and Research Training:	
Name of the Partner University Dean of Research or equivalent:	

AGREEMENT with ST Microelectronics

dated _____.

Between:

STMicroelectronics Private Limited, a company duly organized and existing under the laws of India, having its office at Plot no. 1, Knowledge Park III, Greater Noida (UP) – 201308 hereinafter referred to as “**ST**”

And

Indraprastha Institute of Information Technology, a university duly created and existing under the laws of the Government of Delhi, having its campus in Okhla Industrial Estate, Phase III, New Delhi – 110020, hereinafter referred to as “**IIITD**”.

Hereinafter referred collectively to as the “**Parties**” or individually to as the “**Party**”.

The Parties are proceeding on the following understanding:

WHEREAS ST has experience, capabilities and skills in the field of microelectronics and embedded systems design, production and related activities.

WHEREAS IIITD has educational and research capabilities and experience in the fields of Electronics & Communication Engineering and Computer Science & Engineering.

WHEREAS the Parties, in order to exploit such respective capabilities, agree to have cooperation, on the basis outlined in this Agreement, (hereinafter referred to as the “**Partnership**”) in the **Fields** including, but not restricted to the list given in the **Appendix**.

This agreement, when executed by us, will evidence the mutual agreement between the Parties.

Now, therefore, it is hereby agreed as follows:

1. Definitions

- **ST Affiliates:** means any company owned or controlled directly or indirectly now or hereafter by **ST**. For the purpose of the interpretation of this definition, “Control” means legal, beneficial or equitable ownership, directly or indirectly of more than fifty percent (50%) of the equity of such company or the ability to elect a majority of the Board of Directors or a similar governing body.
- **Intellectual Property Rights (IP Rights):** means all rights in any invention, patent, discovery, improvement, utility model, copyrightable work whether or not registered, industrial design, mask work, algorithm, data structure, trade secret or know-how, confidential information, or any idea having commercial value
- **Patents** means all classes or types of patents (originals, divisionals, continuations, continuations-in-part, provisionals, reissues), including but not limited to patents for inventions, design patents and utility models, and all applications for the aforementioned in all countries in the world.

- **Services:** shall mean the activities/projects to be performed by **IITD** and **ST**, within the frame of the **Partnership**.
- **Field:** shall mean the detailed description of the area of investigation as identified by **IITD** and **ST** for each research project under the **Partnership**.

2. Objects

The **Partnership** shall be limited to the “MTech in ECE with Specialization in VLSI and Embedded Systems” at IIT-Delhi (hereafter called the “MTech programme”), and is based on the following activities:

- 2.1 Creating and conducting a M Tech programme to expose the students to practical R&D scenario in semiconductor industry.
- 2.2 To facilitate proliferation of VLSI Design & Embedded Systems knowledge in **IITD** and **ST** by undertaking research projects with the involvement of **IITD** faculty and **ST** employees.
- 2.3 To provide opportunities to young **ST** employees to pursue higher studies.
- 2.4 To provide hands-on experience to **IITD** faculty members on industrial design issues.
- 2.5 Conducting workshops, seminars, lecture series and conferences.

3. ST Responsibilities and Covenant

- 3.1 **ST** will identify the resource persons for offering the courses allocated to **ST** under the **Partnership**.
- 3.2 All students admitted to this M Tech program will be eligible for getting internships at **ST** during their second year, based on their performance in the courses taken by the students in the first year and subject to their clearing the selection criteria as defined by **ST**
- 3.3 **ST** will make best possible effort for exposing the students to latest tools and technology during the internship.
- 3.4 For all research projects executed under this **Partnership**, **ST** will decide whether to take the design, if applicable, to silicon or not. Fabrication costs will in such cases be borne by **ST**.

4. IITD Responsibilities and Covenant

- 4.1 **IITD** agrees to make its best efforts to create the necessary laboratory facilities as advised by **ST** for supporting the research projects.
- 4.2 **IITD** will invite representatives from **ST** to take part in the counselling/orientation of the students at the time of admission.
- 4.3 In case **IITD** desires to involve some other industry/company as partner in this program, the decision to do so will be taken in consultation with **ST**.

5. Joint Responsibilities and Covenant

- 5.1 A joint committee consisting of **ST** and **IIITD** representatives will decide the courses to be taught under the M Tech program by **ST** faculty.
- 5.2 **IIITD** and **ST** will encourage joint supervision of projects done by **IIITD** students. If any of such projects involve the thesis work of a student, **ST** will permit the person from **ST** associated with the project to be one of the guides for the thesis work of the student, and **IIITD** will in turn make provisions for having such guides from **ST** for the students.

6. Partnership organizations

The Parties undertake to organize the **Partnership** under this Agreement keeping in mind the following parameters:

- decision making
- project approval
- periodic reporting on the project's performance

7. ST Affiliates

IIITD acknowledges that as per **ST** group organization and structure all **ST** Affiliates shall have any and all rights granted to **ST** under the **Partnership**.

8. Intellectual Property Rights

- 8.1 All IP Rights owned or controlled by either Party before the effective date of the Agreement (Background IP) shall remain under the sole ownership of such Party.
- 8.2 The ownership of IP Rights originated, discovered or developed through research projects defined by **ST** and performed by **IIITD** students during their internships at **ST** (Foreground IP) will be exclusively owned by **ST**.
- 8.3 **ST** will sustain the costs of any patent applications filed and of patent prosecutions for each potentially patentable invention originating from the Partnership..
- 8.4 **ST** agrees to pay **IIITD** a sum of USD 1500 (subject to local taxes as applicable) for each invention during internship (as mentioned in 8.2) which **ST** protects through patent applications.
- 8.5 In case of potentially patentable inventions, if **ST** decides to waive any ownership right related to any such IP in favour of **IIITD**, **ST** will inform **IIITD** who may at its own expenses proceed to the filing and prosecution of the patent application. Nevertheless **IIITD** will grant a royalty-free, perpetual license to **ST** under all such IP Rights. .
- 8.6 In addition to the research projects defined by **ST** (ref. Clause 8.2), **IIITD** faculty members can also define and guide research projects to be executed by **IIITD** students at **ST** premises. For all such research projects, **ST** will provide infrastructural support in terms of hardware, software license and design kits. The ownership of IP Rights originated, discovered or developed through such research projects defined by **IIITD** will be exclusively owned by **IIITD**. However, under all such IP Rights, **IIITD** will grant a royalty-free and perpetual license to **ST**.

- 8.7 **IIITD** is granted the non-exclusive right to use any ST intellectual property Rights which are generated under the **Partnership** only for academic research and teaching. However, any disclosure to other people than those involved in the project or any publication will be done only after filing by ST of the patent application in case an invention is going to be protected through patent application.
- 8.8 **IIITD** shall promptly provide to **ST** a written description of each new Invention (“Invention Disclosure”) originated or developed by **IIITD** personnel under the **Partnership**.
- 8.9 If **IIITD** needs to exploit the Foreground IP, originated by **IIITD** employees or jointly, outside the specified **Fields**, **ST** agrees to evaluate the request in a reasonable time. Whenever possible, **ST** will grant to **IIITD**, after negotiation on a case by case basis and under agreed favourable conditions, for the duration of the protection of the Intellectual Property rights, a non-exclusive and transferable world-wide license to have access to and to use the Foreground IP, outside the specified **Fields**.
- 8.10 **ST** shall acknowledge in writing the inventorship of faculty and students as identified by **IIITD** in any publications or patents and/ or other intellectual property that may emerge under the **Partnership**.
- 8.11 Each Party will grant the other Party a non-transferable time-limited, royalty-free, license under its own background if necessary for the completion of any project started under the **Partnership**.
- 8.12 For avoidance of doubt, no Rights are granted or assigned by either party to the other party for any intellectual property or patents generated outside the specified **Fields**.

9. Confidentiality

- 9.1 Confidential Information shall mean any item or information including but not limited to electrical/electronic schematic and circuit diagrams, documentation, specifications, formulas, manufacturing processes, know-how, computer programs, technical descriptions and other technical and economic data, records and information pertaining to the **Partnership**.
- 9.2 Information generated by one party will be treated as confidential information belonging to that party. Information generated within the **Partnership** will be treated as the confidential information of all of the parties. All confidential information shall be marked as “confidential” on any written document.
- 9.3 In case one Party intends to disclose to a third party or disclose orally or publish in public conference or journal the existence of this Partnership or any confidential information generated during the **Partnership**, the Party shall require prior written approval from other Party. The necessary decision would be provided by other Party within **3 working weeks** of submission of the details to be disclosed by first Party.
- 9.4 The obligations herein in relation to each item of Confidential Information of either Party shall subsist for a period of five (5) years from the date of its disclosure, notwithstanding any termination of the Agreement.

10. Liabilities

- 10.1 In no event shall either Party be liable to the other Party for any special, indirect, punitive, incidental or consequential damage caused by any breach of its obligations to the other arising out or in connection with any Project under the **Partnership**.
- 10.2 In no event shall either Party be liable to the other in respect to any events arising out or in connection with any Project under the **Partnership** for all causes of action, unless in case of gross negligence or wilful misconduct.

11. Termination

- 11.1 This Agreement may be terminated or suspended mutually by either side after giving **six months' notice**. However, agreed responsibilities towards students need to be fulfilled.
- 11.2 Immediately upon termination, each Party shall provide to other Party all descriptions of final deliverables and any other IP/copyright material pertaining to the activities under the **Partnership**.
- 11.3 The terms as mentioned in this Agreement shall continue in force until terminated according to these terms.

12. Dispute Resolution

- 12.1 In the event of a dispute arising out of or in connection with this Agreement, including any question regarding its existence, validity or termination, the Parties shall attempt to resolve such dispute through amicable negotiation in good faith and by all reasonable and appropriate means.
- 12.2 If the Parties are unable to resolve the dispute, then it shall be referred to and finally resolved by arbitration in New Delhi, in accordance with the Rules of Conciliation and Arbitration of the International Chamber of Commerce for the time being in force, which rules are deemed to be incorporated by reference in this article. The language of the arbitration shall be English.

13. General

- 13.1 This Agreement is intended to remain confidential. No public release or disclosure of information concerning its existence and/or content and/or the activities described in this agreement is made by either Party, without the prior written consent of the other Party. Any further activities pertaining to or anyhow related to the **Partnership** and the services deriving there from shall be subject to the signature of a NDA to be executed between the Parties at least on the signature of the Agreement.
- 13.2 Neither Party shall assign or transfer any of its rights or obligations hereunder without the prior written consent of the other Party; notwithstanding the foregoing **ST** may assign this agreement to any **ST** Affiliate.

- 13.3 Each Party will bear its own cost relating to the discussions covered by this Agreement.
- 13.4 Except for this section “13. General” the use of the auxiliaries “will” and “shall”, if any, in the rest of the agreement, does not create any kind of obligations to the Parties, such auxiliaries shall be interpreted as declaration of intention.
- 13.5 The effective date of the Agreement will be the date of execution indicated below.
- 13.6 This Agreement shall be valid from the effective date for period of **three years** subject to a joint yearly review.
- 13.7 Neither of the Parties shall use the name, image, trademark, logo, icon, or other symbol of the other Party without prior written approval of the other Party in any public announcement, news release or advertising relating to this Agreement.

ST and IITD have caused this Agreement to be executed as on _____ by their duly authorized representatives

STMicroelectronics Private limited

**Indraprastha Institute of
Information Technology,**

DATED:

APPENDIX

Brief listing of topics of interest relating to which research projects would be defined are as follows (though not limited to):

- CMOS amplifiers,
- Clock generator and synchroniser circuits, PLLs and DLLs, Equalisers,
- DAC architectures, Switched capacitor circuits, ADC architectures, SRAM architecture and cell design, DRAM architecture and cell design,
- Memory testing,
- CMOS design capture and verification, Synthesis and timing, Floor planning and power plan,
- ATPG, Design for testability, BIST,
- Performance and power aware design, Leakage current control techniques, Interconnects and noise,
- Processor performance metrics, Parallel architectures, Asynchronous design,
- Digital audio compression, Image compression, Video coding and compression.

- Language theory and implementation
- Real time theory and implementation
- Applied mathematics (graph theory, optimization theory etc.)
- Software engineering paradigms and methods
- Advanced multimedia algorithm development & architecture mapping of the same.
- Digital Media content metadata extraction. Methods for their representation and processing to support efficient and user friendly content archival/search & retrieval.
- Pattern recognition (image/speech), Sensory research
- Video analytics

UGC Report/Recommendations for the Senate Meeting on 19th June 2013**UG items to be decided:****1. Basic Electronic Circuits backlog**

In AY 2012/2013 Basic Electronic Circuits has been replaced by Introduction to Engineering Design in the first year of the BTech curriculum. UGC proposes to allow backlogger to register for IED to clear BEC.

Within UGC there was no uniform opinion whether Linear Circuits (a second year core course of Btech(ECE)) should also be permitted to clear BEC. The main objection to the latter was that it might “dilute the ECE course”.

2. Credits of IS/IP/UR exceeding the allowed limit

Two final year BTech students (**Daksha Yadav and Naman Kohli**) had registered with approval for an online course which did not start as announced. This became clear after a few weeks into the semester, only. Dr Mayank Vatsa who was supervising them requested UGC to convert the online course into a regular course which he would teach. UGC saw some principal problems with such a conversion (course approval not done, possible misuse to increase the teaching load by instructors,..) and approved instead to let these two final year students do the subject as an additional IS course. It also suggested to look at the problem of non starting online courses again. Apparently, that is a frequent problem.

3. Branch Transfer

Applications were invited from the first year students during 17-26 May, 2013, for the Branch Transfer from CSE to ECE or vice versa. Students interested in Branch Transfer:

From CSE to ECE – 02 students.

From ECE to CSE – 15 students.

For the transfer from ECE to CSE grades of IP and DSA have to be B- or better. There is no respective requirement for the transfer from CSE to ECE.

The senate decide on the students being allowed to change their branch.

UGC reports:**1. No repeaters of BTech2012**

Out of the BTech2012 batch, one student failed two of the core courses, and 14 students failed one core course during their first year at IIITD. All others passed all the core courses apart from two students who left the program and one who took a medical leave.

2. **Nominations for BTech awards:**

All Round Performance Medal

The committee for selecting the best all round performer met on 15 May 2013. After due deliberation, the committee suggests to give the **all round performance medal** to **Raghav Sethi**.

As a class representative for two successive years, Raghav was a responsible and active member of the student council. He was commended by the institute for his TA work for the IP course. He has taken part in various inter-IIIT tournaments and won multiple medals. He was instrumental in the design team and helped design the logo, color scheme and stationary of the institute. All the committee members appreciated the diversity of his activities which he always pursued with full involvement and a high sense of responsibility, and the fact that he was still able to keep a very good academic record.

Best BTP Awards

The committee for selecting the best BTP in the engineering and research track met on 15 May 2013. All the projects were presented and all students involved took part in their BTP presentation. The committee evaluated the BTPs based on the effort put in by the students, the complexity & completeness of the work, the quality of the presentation and the understanding of the students shown in the question & answer part, the impact to society & environment (for the engineering track), and the embedding into related work/background knowledge (for the research track).

The committee recommends to the director following items:

Engineering Track:

No award to be given, as there was no nomination.

Research Track:

No award to be given, as the research was not convincing or outstanding by any of the three nominees.

No BTP was done in the entrepreneur track.

3. **Migration to the Dual Degree Program**

In response to the invitation for migration to Dual Degree Program, four students have applied (the prospective Thesis Guide's name is in parenthesis), **PGC will look into the applications:**

1. Digvijay Singh-2009020 (Dr. Pushpendra Singh)
2. Devika Sondhi-2010028 (Dr. Pushpendra Singh)
3. Shayan Lahiri-2010078 (Dr. P.B. Sujit)
4. Yash Lamba-2010097 (Dr. Ashish Sureka)

DAC recommendation

Rollno	Name	Category	Punishment	Punishment-1	Punishment-2
2011102	Shikhar Singhal	A	Suspended for two semesters from the Institute.	Debarred from the hostel and not allowed to bring a motorized vehicle to the Institute.	
2010014	Anirudh Chakorborty	B	Suspended for one semester from the Institute.	Debarred from the hostel.	
2012027	Ashish Rawat	B	Suspended for one semester from the Institute.	Debarred from the hostel.	
2011039	Deepak Wali	B	Suspended for one semester from the Institute.	Debarred from the hostel.	
2009009	Akshit Nanda	C	Conferment of degree be withheld until further notice, caution money/security deposit be forfeited.	To carry out work assigned by the Institute for a period of 2 months.	
2012006	Abhishek Gautam	D	One semester suspension from the Institute, and debarred from the hostel.	The semester-suspension to be kept abeyance, and will be activated if there is any activity attracting disciplinary action.	
2010003	Abhishek Sharma	D	One semester suspension from the Institute, and debarred from the hostel.	The semester-suspension to be kept abeyance, and will be activated if there is any activity attracting disciplinary action.	
2009056	Aditya Kumar	D	One semester suspension from the Institute, and debarred from the hostel.	The semester-suspension to be kept abeyance, and will be activated if there is any activity attracting disciplinary action.	Not allowed to use the campus network with their laptop or other mobile devices until further notice.
2011021	Ankit Kumar	D	One semester suspension from the Institute, and debarred from the hostel.	The semester-suspension to be kept abeyance, and will be activated if there is any activity attracting disciplinary action.	
2011034	Avi Kathpalia	D	One semester suspension from the Institute, and debarred from the hostel.	The semester-suspension to be kept abeyance, and will be activated if there is any activity attracting disciplinary action.	
2011043	Dhruv Kumar	D	One semester suspension from the Institute, and debarred from the hostel.	The semester-suspension to be kept abeyance, and will be activated if there is any activity attracting disciplinary action.	
2011074	Prakhar Gupta	D	One semester suspension from the Institute, and debarred from the hostel.	The semester-suspension to be kept abeyance, and will be activated if there is any activity attracting disciplinary action.	Not allowed to use the campus network with their laptop or other mobile devices until further notice.

2011093	Rudresh Pandey	D	One semester suspension from the Institute, and debarred from the hostel.	The semester-suspension to be kept abeyance, and will be activated if there is any activity attracting disciplinary action.	
2012157	Sankalp Mehar	D	One semester suspension from the Institute, and debarred from the hostel.	The semester-suspension to be kept abeyance, and will be activated if there is any activity attracting disciplinary action.	
2011107	Shubham Pandey	D	One semester suspension from the Institute, and debarred from the hostel.	The semester-suspension to be kept abeyance, and will be activated if there is any activity attracting disciplinary action.	Not allowed to use the campus network with their laptop or other mobile devices until further notice.
2011109	Siddhant Jain	D	One semester suspension from the Institute, and debarred from the hostel.	The semester-suspension to be kept abeyance, and will be activated if there is any activity attracting disciplinary action.	
2012120	Vivek Raj	D	One semester suspension from the Institute, and debarred from the hostel.	The semester-suspension to be kept abeyance, and will be activated if there is any activity attracting disciplinary action.	Not allowed to use the campus network with their laptop or other mobile devices until further notice.
2009018	Chirag Gupta	E	Conferment of degrees be withheld until further notice.	Caution money/ security deposit be forfeited and asked to carry out work assigned by the Institute for a period of 1 month.	
2009039	Rohit Dixit	E	Conferment of degrees be withheld until further notice.	Caution money/ security deposit be forfeited and asked to carry out work assigned by the Institute for a period of 1 month.	
2011092	Romil Bhardwaj		NO Charges		
2011100	Saurabh Arya		This has been viewed seriously and you have been warned not to repeat in future, failing which disciplinary action will be taken.		
2011095	Sahil Dhanda		This has been viewed seriously and you have been warned not to repeat in future, failing which disciplinary action will be taken.		
2011098	Sarthak Bajaj		This has been viewed seriously and you have been warned not to repeat in future, failing which disciplinary action will be taken.		
2011031	Arhan Sibal		Any repetition of such incidence will be viewd seriously		

Observation/Comments of Sub-Committee of DAC

Note to Senate

Three students – Anirudh Chakravorty, Sikhar Singhal and Ashish Rawat through his father APS Rawat – appealed to the Director against the disciplinary action taken.

1. **Sikhar Singhal**, in his appeal, did not contest the offence as determined by DAC in his case nor did he provide any other material for reconsideration. His appeal is simply to reduce the punishment.
2. **Anirudh Chakravorty** admits to consumption of marijuana in the Institute but does not agree to “facilitating marijuana use in hostels and possible involvement in marijuana procurement”. He would like to be punished only for the lightest of the offence – “complicit in some marijuana related incidents”. He has given 3 arguments in support of his appeal –
 - a. Some students wanted an examination postponed and he opposed it therefore some of these students may have reported his name.
 - b. Sikhar Singhal gave his name as someone who provided marijuana in his initial statement by mistake and later Shikhar retracted it and therefore he should not be charged with “possible marijuana procurement”
 - c. He contests the charges by 4 students of threatening them if they implicated Anirudh in the incident.
3. **Ashish Rawat’s** father has appealed and his main argument is that his son, Ashish, admitted to going with Sikhar Singhal to NFC Market to buy marijuana under pressure from senior students. Now he claims Ashish never went to buy the stuff. Therefore he is seeking a lighter punishment.

Our Views

1. No comments.
2. The argument (2a) is not relevant. Even if the complaint was made due to the reason given, Anirudh has admitted to smoking marijuana. The DAC did not meet the original complainant nor asked him to give his views. As far as (2b) is considered, we are of the view that Shikhar’s first statement is more reliable. The retraction was made by Shikhar on 30th April. DAC records show a statement of Abhishek Kumar Gautam of 29th April stating that Ashish Rawat told him that “in DAC meeting Shikhar will say he accidentally said the name of Anirudh...”. This ties together with the testimony of 4 students (who do not benefit due the accusation they made against Anirudh) about Anirudh threatening them (2c). We also see the threats as obstruction of DAC hearing as it was intended to prevent facts being presented to DAC.
3. We had detailed questioning of Ashish on this aspect. Ashish and Shikhar (together as well as individually) both stated that Ashish went with Shikhar on Shikhar’s scooter to NFC market for purchasing marijuana. There is no reason to believe that his new statement is true.