

## **Agenda for the 47<sup>th</sup> meeting of the PGC to be held on 27<sup>th</sup> May 2020, 2.30 - 4:00 PM**

Confirmation of the minutes of the 46th meeting of the PGC held on 27<sup>th</sup> April, 2020.

1. To discuss regarding request for fee waiver by Mr. Ajay Fuloria (offered admission as a sponsored PhD student) under the guidance of Dr Arun Balaji Buduru. PGC has earlier considered the request and was not in favor of giving fee-waiver, the same was communicated to the student and the advisor. Dr Buduru requested to present his view to PGC to re-consider the fee waiver request for the student.
2. To discuss regarding the PG diploma program in partnership with IBM. Given the current situation, the proposal is to take the program up online with a substantial reduction in the program fees. The changed program document is attached as Annexure I. It may be noted that there is no change in the program curriculum.
3. To discuss regarding below points related to MTech internship
  - Internship guidelines related to Non-GATE students
  - Internship guidelines for 2019 batch MTech students
  - Eligibility CGPA cut-off for M.Tech. Internship(Draft proposal attached)
4. To discuss regarding Honors in MTech program (below are the proposed requirements which are mostly in line with our BTech program).

M.Tech. students can graduate with Honors degree, requirements for which may be as follows:

- 1) The students must earn an additional 8 discipline credits
- 2) The student must have done 16 credits of Thesis
- 3) At graduation time students must have 8 CGPA (As per the records of last 4-5 years, around 50% students graduate with >8 CGPA)
- 4) The student must not have any F grade (This is not there in UG program. Since in M.Tech. students have to do less courses, this might be added. Just a suggestion)

5. To consider addition of VLSI Specialisation courses:

(i) ECE Dept has recommended below

**to remove:**

- VLSI Design & Test Flow
- Memory Design

**to add:**

- VLSI Design Flow
- Digital Hardware Design
- Low Voltage Analog Circuit Design
- Verification and High-Level Synthesis of VLSI Designs
- Memory Design and Testing
- Intelligent Applications Implementation on Heterogeneous Platform
- Introduction to Nanoelectronics

6. CB Department has recommended to consider addition of BDS course under eligibility of MTech CB.

"we have an agreement on allowing BDS too as a valid program as part of eligibility criteria for the M.Tech. in Computational Biology program."

**Present criteria:**

B.Tech/BE in CS/IT/Math-and-Computing	<b>or</b>
B.Tech/BE in any other discipline	<b>or</b>
MCA	<b>or</b>
B.Pharma	<b>or</b>
MBBS	<b>or</b>
MSc. (Physics, Chemistry, Mathematics, Computer Science, Biochemistry, Microbiology, Biotechnology, Biophysics, Bioinformatics, Biomedical sciences)	

**7. Thesis Defense/Scholarly Paper Report Submission name :**

In a recent decision of the PGC and subsequent approval of the Senate, it was decided to grade Thesis/SP/CapP. During the semester, current practice will be followed, where S/X will be awarded will be awarded for multiple credit registration. When the student is expected to complete the min credit, in that particular semester S/he will be registering for the defense or report submission. Following are the proposed name:

M.Tech. Thesis Viva (16 Credits)  
Scholarly Paper Report (4/8 Credits)  
Capstone Project Report

**8. Any other item with permission of Chair PGC.**